

BRCM24C02SC

SOP-8

2 Kbit

I²C

1.7V

2.5~5.5V

1Mhz

1.7~2.5V

400Khz

CMOS

400uA

1.6mA

8

128

5ms

100

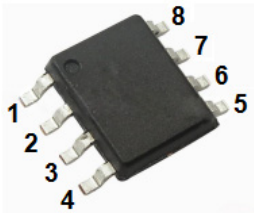
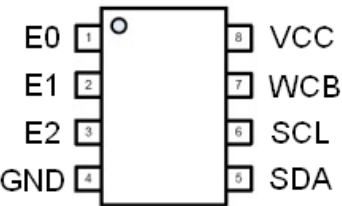
100

ESD

HBM

6KV

+/-200mA;



Pin	Name	Type	Description
1	E0	Input	
2	E1	Input	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

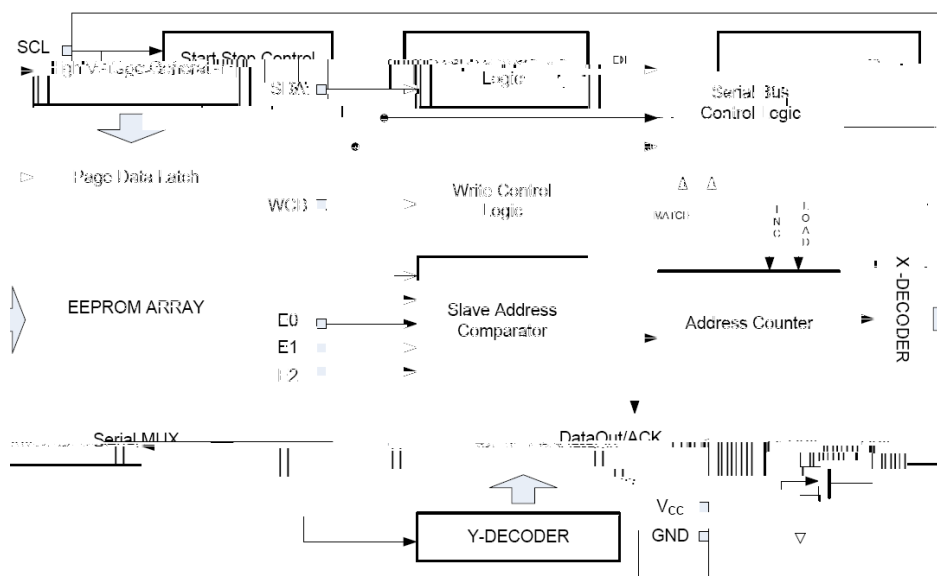
			mA
			V

		Ñ				

[illegible]

—

[illegible]

[illegible]

H

SDA

SDA

SDA

SCL

(1)

SCL

(1)

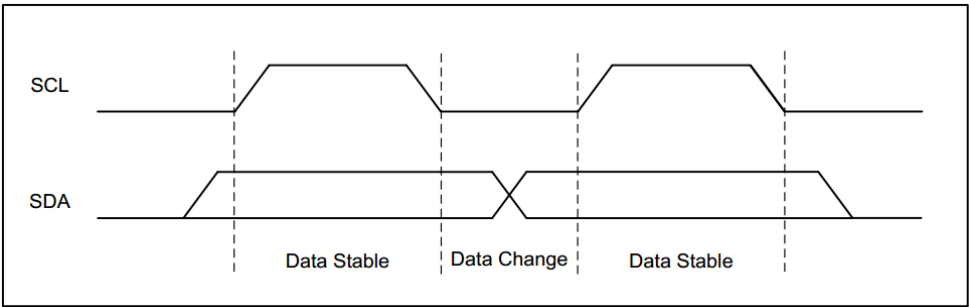


Figure 1 Data Validity

H

SCL

SDA

2

H

SCL

SDA

BRCM24C02SC

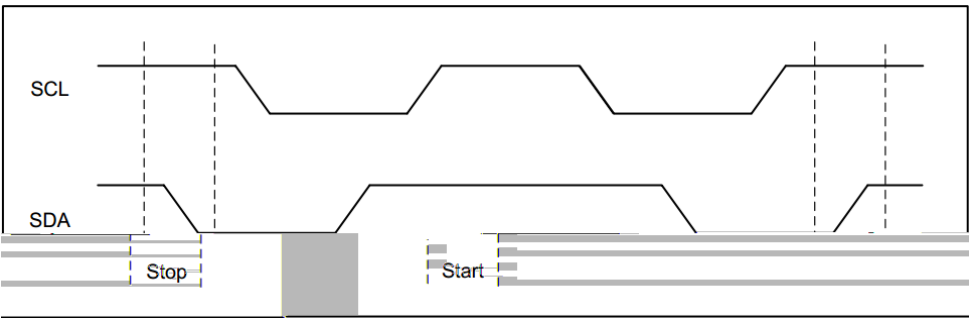


Figure 2 Start and Stop Definition

H

ACK

BRCM24C02SC

BRCM24C02SC

" 0 "

9

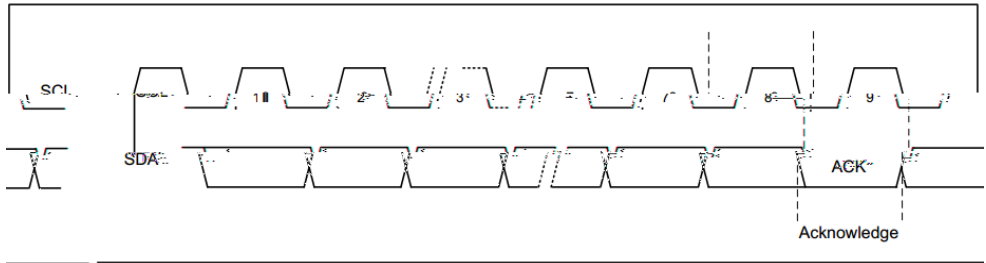


Figure 3 Output Acknowledge

H

BRCM24C02SC (a) (b) (c)

H

(a) (b) (c) 4

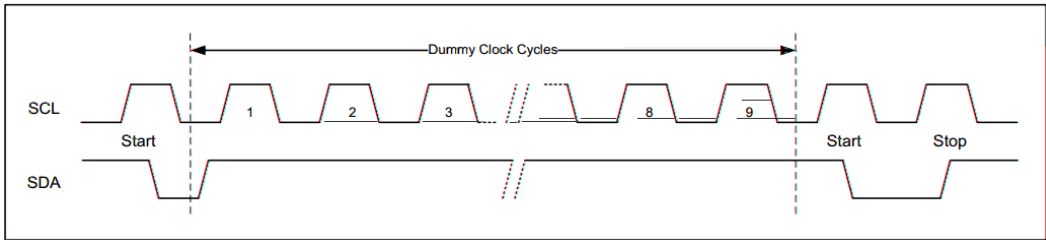


Figure 4 Soft Reset

H

BRCM24C02SC 8 1 1 0

E2,E1E0

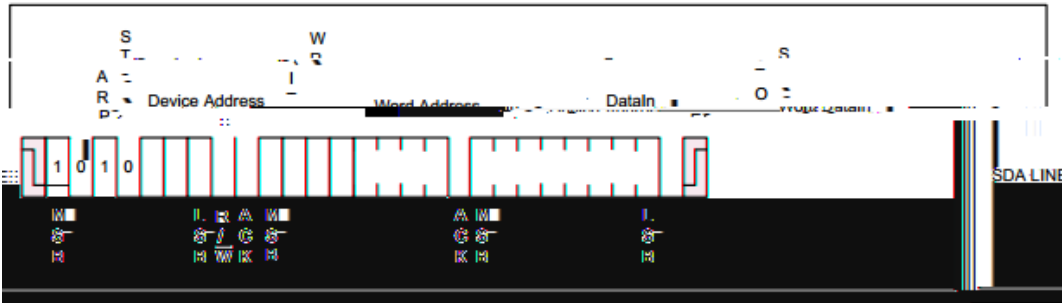
0

8

8/

E2,E1E0

0

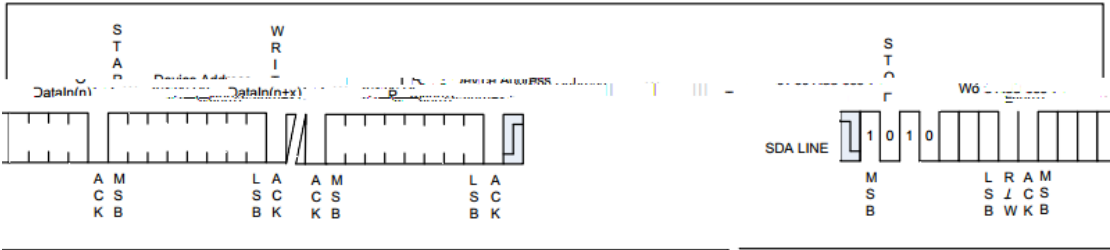


H

BRCM24C02SC

“ 0 ”

BRCM24C02SC



3

BRCM24C02SC

8

H

BRCM24C02SC

/

BRCM24C02SC

" 0 "

H

16

(a)

1011b

(b)

A5 / A4

0

1

A7 / A6

" 00 "

(c)

A3 / A0

NoACK

H

ID

Lock ID

(a)

1011b;

(b)

A6

1

;

(c)

xxxx xx1x

x

0

1

H

/

•
,

/

“ 1 ”

H

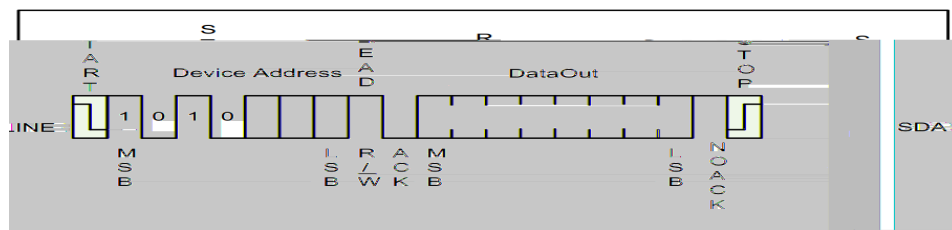
1

/ “ 1 ”

BRCM24C02SC

“ 0 ”

7



H

“ ”

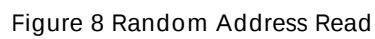
BRCM24C02SC

/

BRCM24C02SC

“ 0 ”

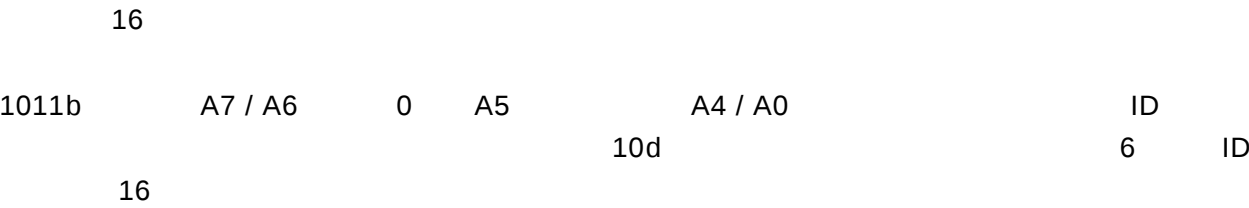
8



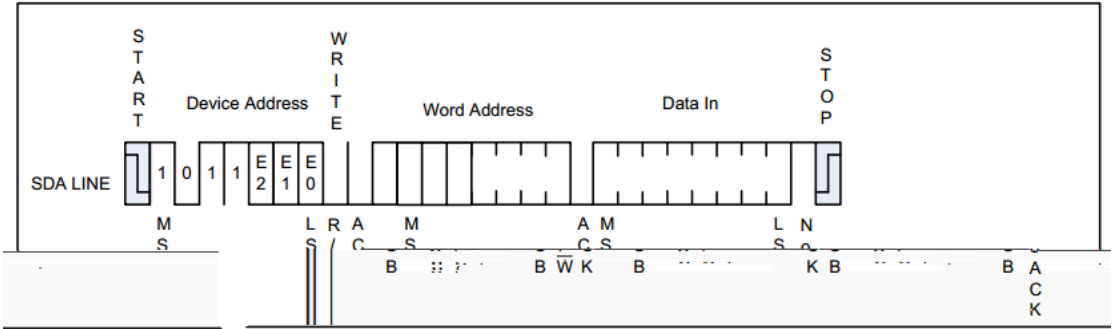
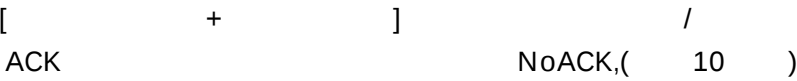
“ 0 ”

Figure 9

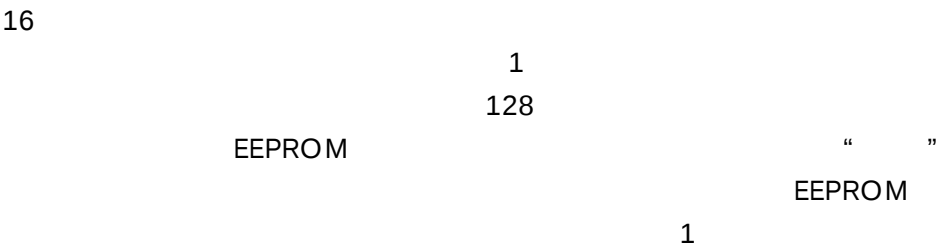
H



H



H



A7 A6 " 10 " 2 " 10 "

80h
 128 16

128 ACK

7

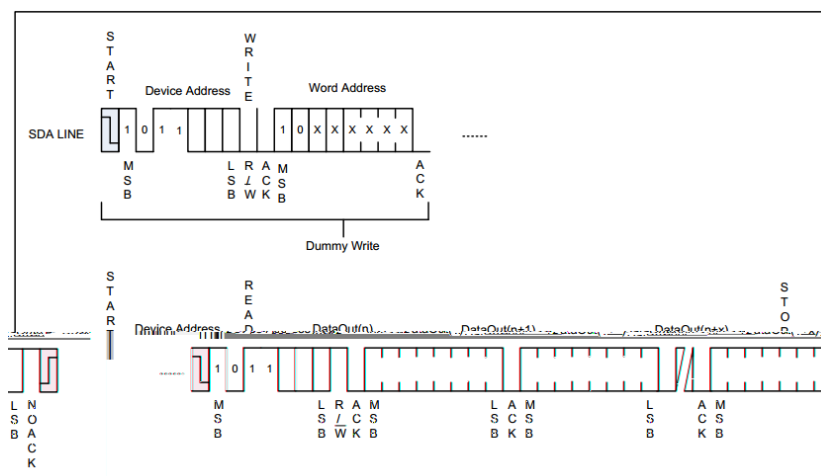
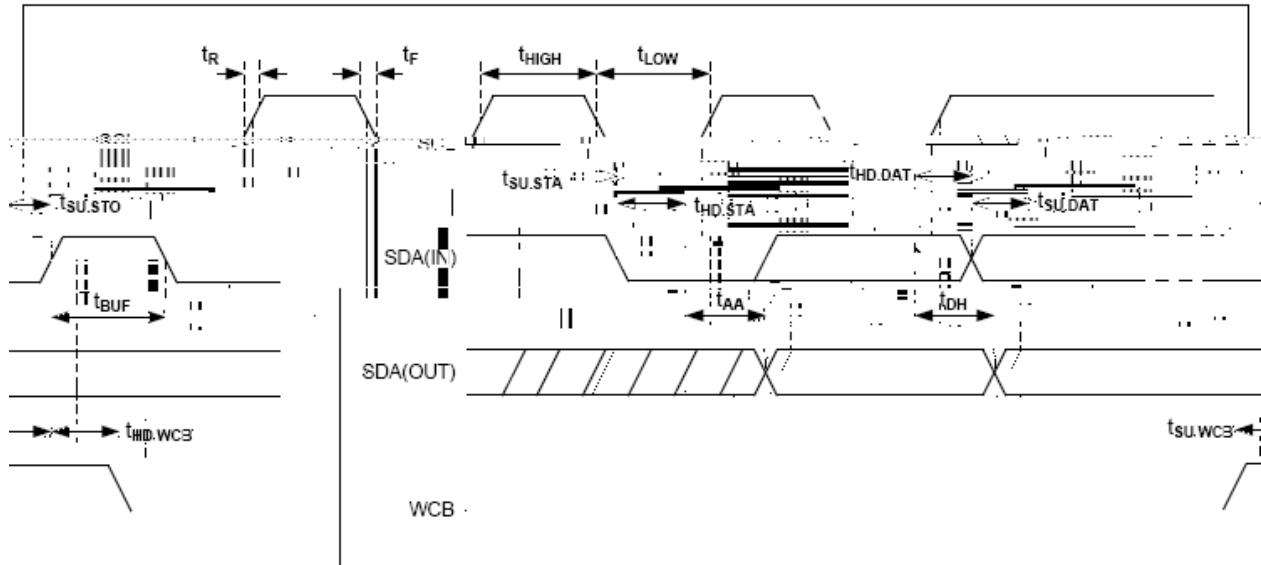
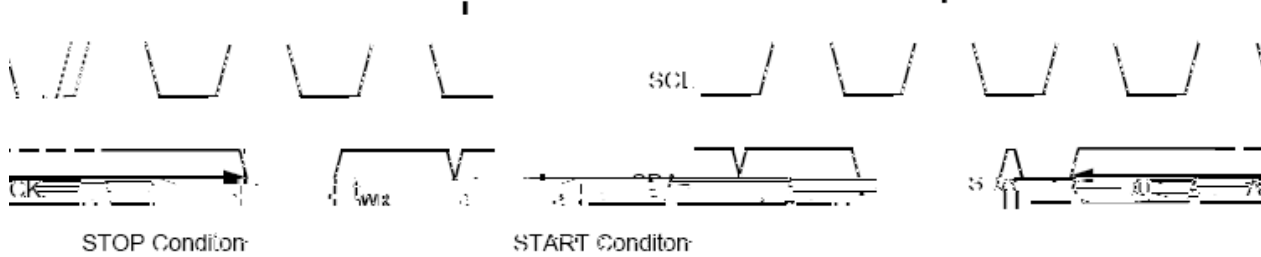


Figure 11 Sequential Read

Bus Timing



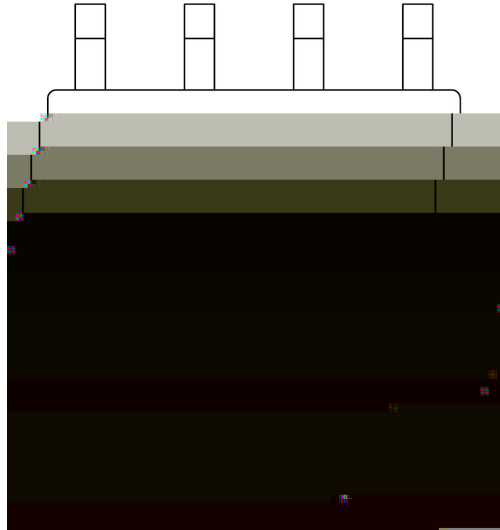
Write Cycle Timing



me from a valid stop condition of a write sequence to the end of the internal clear/write cycle.

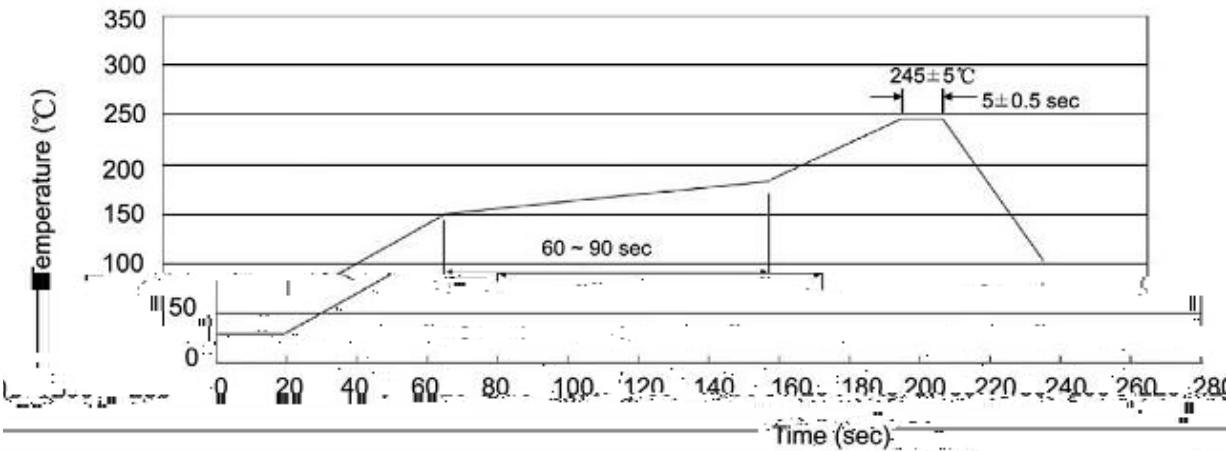
Note: The write cycle time t_{W1} is the ti





24C02

Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

± ± ± ±

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box
SOP/ESOP-8	4,000	2	8,000	6	48,000	13 ×12	360×360×50	380×335×366