

/ Descriptions

SOT-23 PNP Silicon PNP transistor in a SOT-23 Plastic Package.

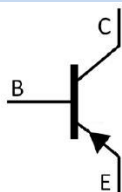
/ Features

2SC5343S

Low VCE(sat), low output capacitance, complementary pair with 2SC5343S.

/ Applications

General small signal amplifier applications.

/ Equivalent Circuit**/ Pinning**

PIN1 Base PIN 2 Emitter PIN 3 Collector

/ h_{FE} Classifications & Marking

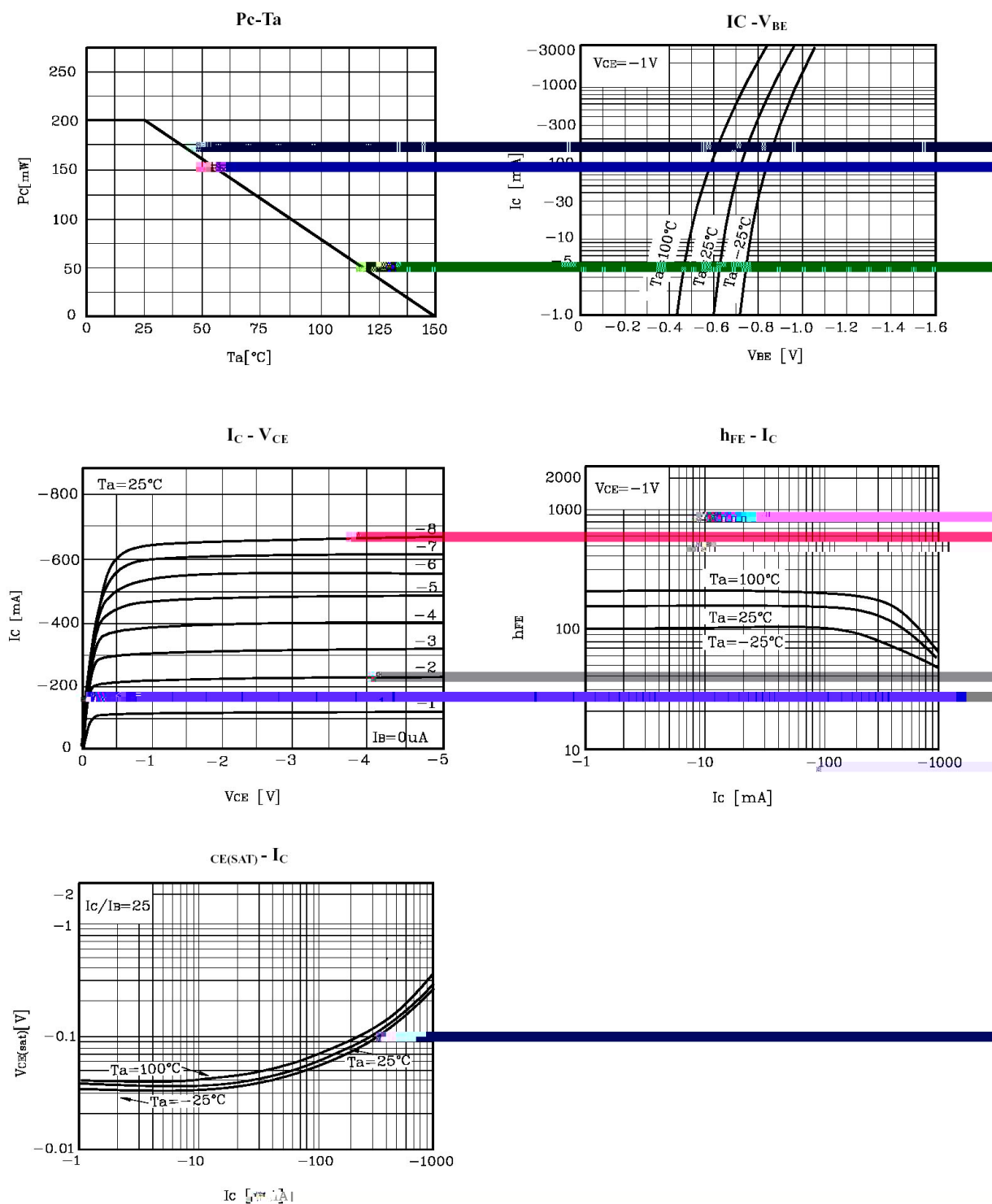
h _{FE} Classifications Symbol	O	Y	G	L
h _{FE} Range	70 140	120 240	200 400	300 700
Marking	HCAO	HCAY	HCAG	HCAL

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-50	V
Collector to Emitter Voltage	V_{CEO}	-50	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current	I_C	-150	mA
Collector Power Dissipation	P_C	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature Range	T_{stg}	-55 150	°C

Parameter	Symbol	Test Conditions
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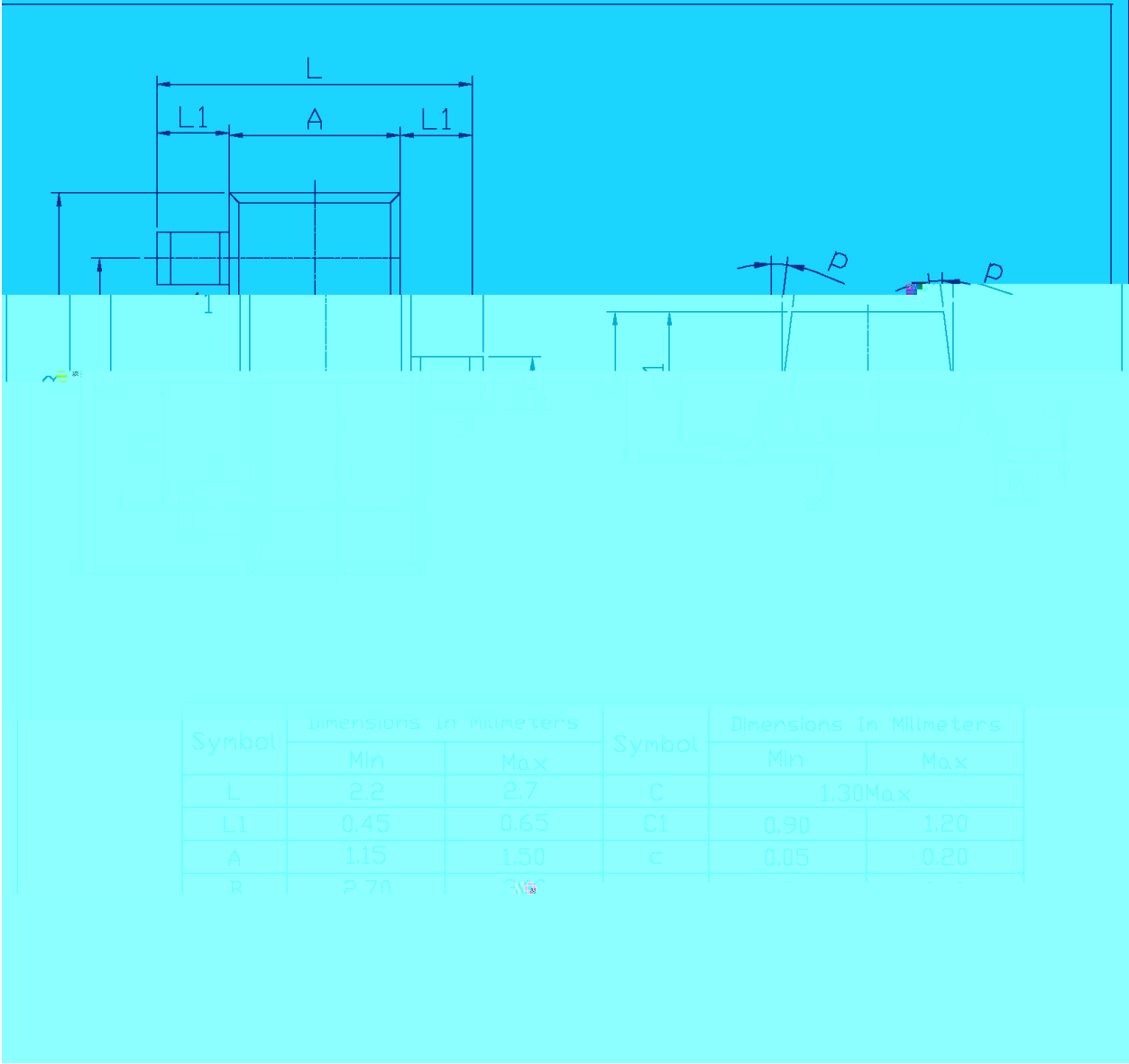
/ Electrical Characteristic Curve



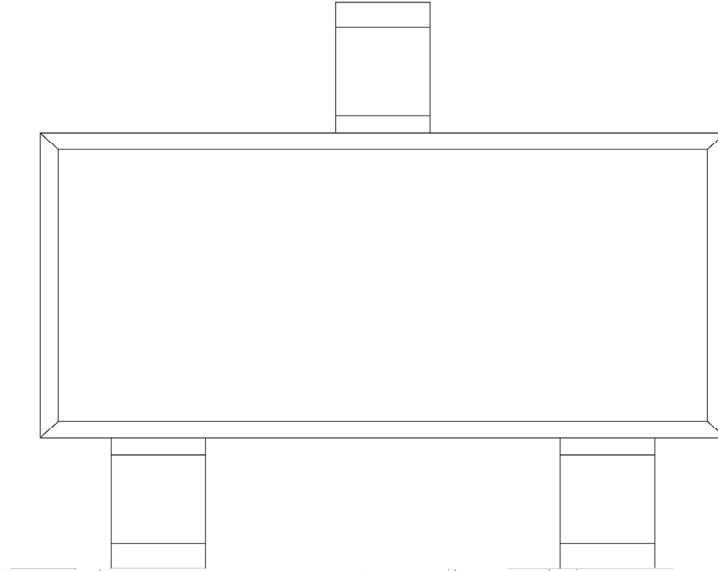
/ Package Dimensions

SOT-23

单位: mm

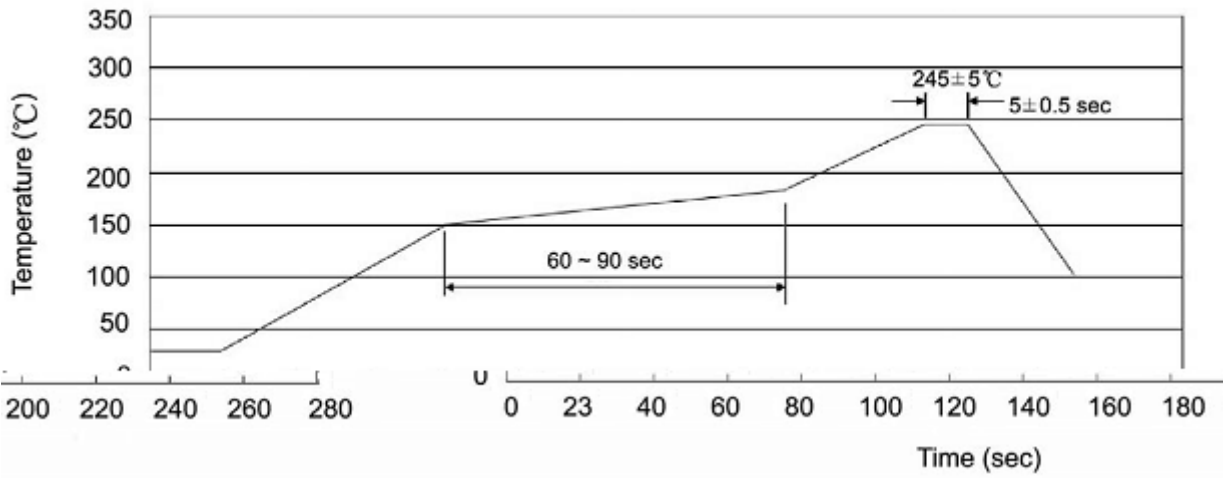


/ Marking Instructions



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Note:

- | | | | | | |
|---|-------|-----|----|-----------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245±5 | | | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | | 2 | 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

260±5 10±1 sec. Temp.:260±5℃ Time:10±1 sec

/ Packaging SPEC.

/ REEL

Units 包装数量 Dimension 包装尺寸 (unit: mm “!†l@d” ps