



A	2019.4.17				
B	2020-5-12	1/3/4	RDSON 60mR 50mR		
C	2020-7-23	11			
D	2020/11/4	1 2			



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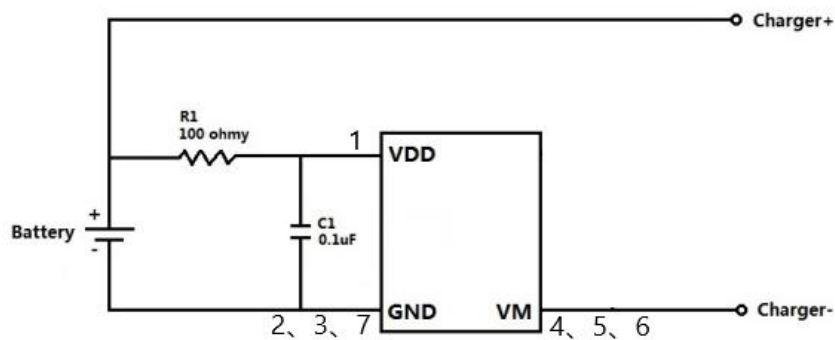
The BRCL3130ZF series product is a high integration solution for lithium-ion/polymer battery protection. BRCL3130ZF contains advanced power MOSFET, high-accuracy voltage detection circuits and delay circuits.

BRCL3130ZF is put into an ultra-small DFN2 2-6L package and only one external component makes it an ideal solution in limited space of battery pack. BRCL3130ZF has all the protection functions required in the battery application including overcharging, overdischarging, overcurrent and load short circuiting protection etc. The accurate overcharging detection voltage ensures safe and full utilization charging. The low standby current drains little current from the cell while in storage. The device is not only targeted for digital cellular phones, but also for any other Li-Ion and Li-Poly battery-powered information appliances requiring long-term battery life.

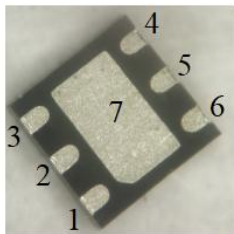


- ◆ Integrate advanced power MOSFET with Equivalent of $50\text{m}\Omega$ $R_{\text{DS(ON)}}$;
- ◆ Ultra-small DFN2 $\times$ 2-6L package;
- ◆ Over-temperature Protection; Overcharge Current Protection; Two-step Overcurrent Detection: Overdischarge Current; Load Short Circuiting.
- ◆ Charger detection function; 0V battery charging function; delay times are generated inside; High-accuracy voltage detection.
- ◆ Low Current Consumption; Operation Mode: $2.8\mu\text{A}$ typ; Power-down Mode: $1.5\mu\text{A}$ typ ; RoHS Compliant and Lead (Pb) Free.
- ◆ Halogen-free Product.

One-Cell lithium-ion battery pack; Lithium-Polymer battery pack.



- Notes:
- (1) The chip power consumption shall not exceed the maximum power consumed by the package.
 - (2) This product has anti-static protection function, but do not exceed the maximum capacity of the product to withstand static electricity.



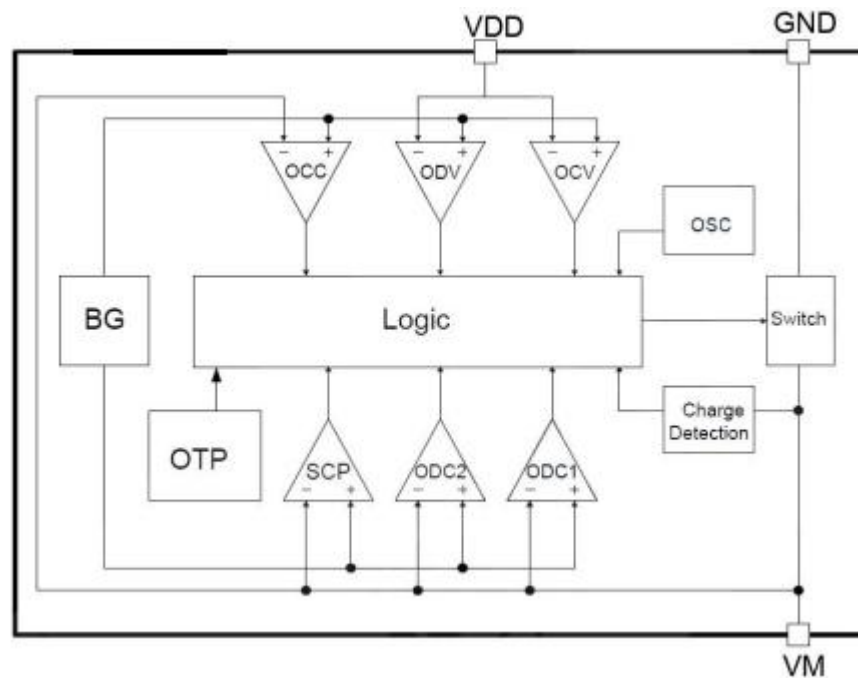
	VDD	Power Supply
	GND	Ground, connect the negative terminal of the battery to this pin.
	VM	The negative terminal of the charger. The internal FET switch connects this terminal to GND.

See Marking Instructions.



/Parameter	/Symbol	/Value	/Unit
V _{DD} input pin voltage	V _{IN}	-0.3 to +6.0	V
V _M input pin voltage	V _{VM}	-6.0 to +10	V
Maximum Junction Temperature	T _J	125	°C
Lead Temperature	T _L	300	°C
Operating Junction Temperature	T _{opr}	-40 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Package Thermal Resistance	R _{θJA}	250	°C/W
	R _{θJC}	130	°C/W
ESD	ESD	2000	V

/Parameter	/Symbol	/Test Condition	/Min	/Typ	/Max	/Unit
Overcharge Detection Voltage	V _{CU}		4.25	4.30	4.35	V
Overcharge Release Voltage	V _{CL}		4.05	4.10	4.15	V
Overdischarge Detection Voltage	V _{DL}		2.30	2.40	2.50	V
Overdischarge Release Voltage	V _{DR}		2.90	3.00	3.10	V
Charger Detection Voltage	V _{CHA}			-0.12		V
Overdischarge Current1 Detection	I _{IOV1}	V _{dd} =3.5V		3.0		A
Load Short-Circuiting Detection	I _{SHORT}	V _{dd} =3.5V		12		A
Current Consumption in Normal Operation	I _{OPE}	V _{dd} =3.5V, V _M =0V		2.80		μA
Current Consumption in power Down	I _{PDN}	V _{dd} =2V, V _M floating		1.50	6.0	μA
Equivalent FET on Resistance	R _{DS}	V _{dd} =3.6V, I _{VM} =1A		50		mΩ
Over Temperature Protection	T _{SHD+}			120		°C
Over Temperature Recovery Degree	T _{SHD-}			100		°C
Overcharge Voltage Detection Delay Time	T _{CU}	V _{DD} =3.6V~4.4V		128		ms
Overdischarge Voltage Detection Delay Time	T _{DL}	V _{DD} =3.6V~2.0V		32		ms
Overdischarge Current1 Detection Delay Time	T _{IOV1}	V _{DD} =3.6V		8.0		ms
Load Short-Circuiting Detection Delay Time	T _{SHORT}	V _{DD} =3.6V		32		μS



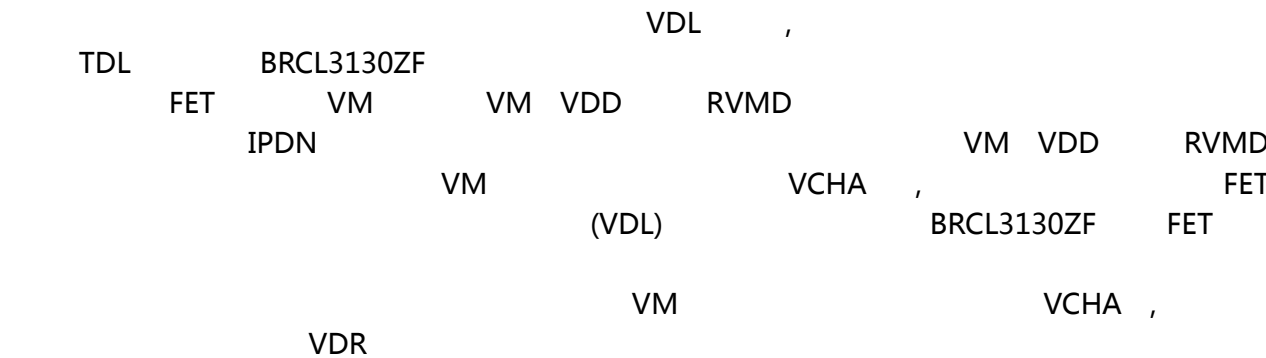
BRCL3130ZF

MOSFET

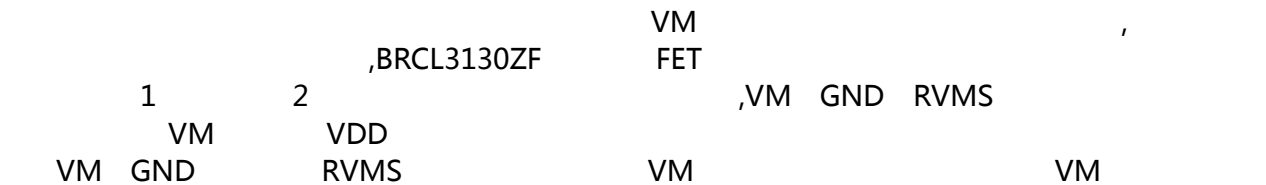
50mΩ

The BRCL3130ZF monitors the voltage and current of a battery and protects it from being damaged due to overcharge voltage, overdischarge voltage, overdischarge current, and short circuit conditions by disconnecting the battery from the load or charger. The peripheral circuit is very simple. The MOSFET is integrated and its $R_{DS(ON)}$ is as low as 50mΩ typical.

If no exception condition is detected, charging and discharging can be carried out freely. This condition is called the normal operating mode.



When the battery voltage drops below the overdischarge detection voltage (VDL) during discharging under normal condition and it continues for the overdischarge detection delay time (tDL) or longer, the BRCL3130ZF turns the discharging control FET off and stops discharging. This condition is called overdischarge condition. After the discharging control FET is turned off, the VM pin is pulled up by the RVMD resistor between VM and VDD in BRCL3130ZF the current of the chip is reduced to the power-down current (IPDN). This condition is called power-down condition. The VM and VDD pins are shorted by the RVMD resistor. The power-down condition is released when a charger is connected and the potential difference between VM and VDD becomes typical or higher, at this time, the FET is still off. When the battery voltage becomes the overdischarge detection voltage (VDL) or higher (see note), the BRCL3130ZF turns the FET on and changes to the normal condition from the overdischarge condition. Note: If the VM pin voltage is no less than the charger detection voltage (VCHA), when the battery under overdischarge condition is connected to a charger, the overdischarge condition is released (the discharging control FET is turned on) as usual, provided that the battery voltage reaches the overdischarge release voltage (VDR) or higher.



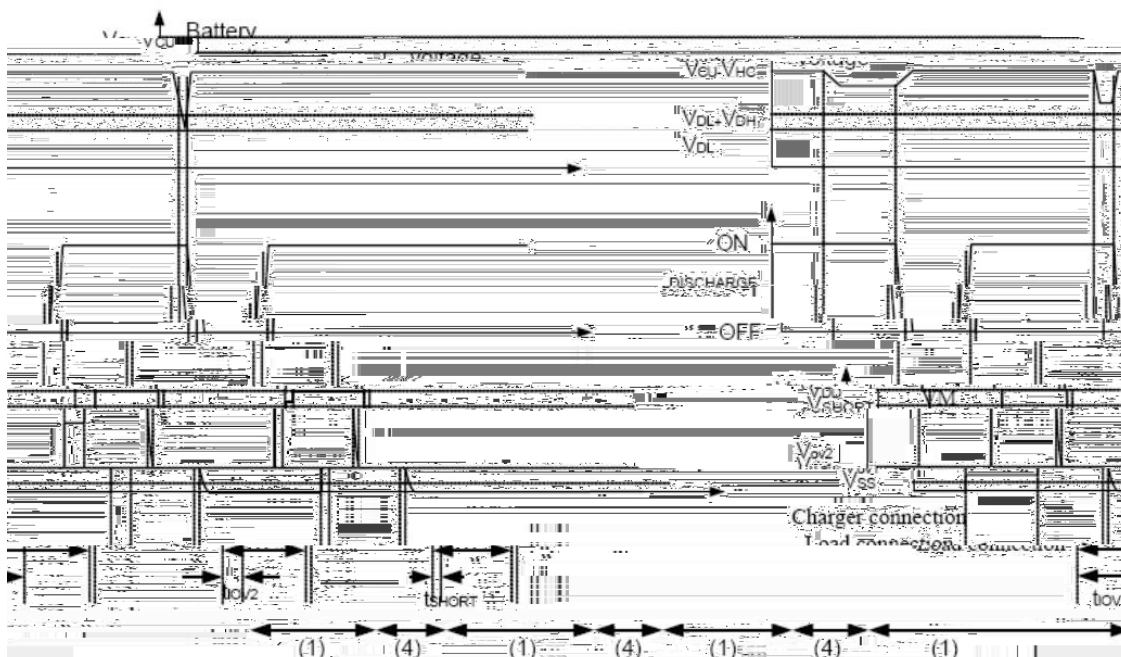
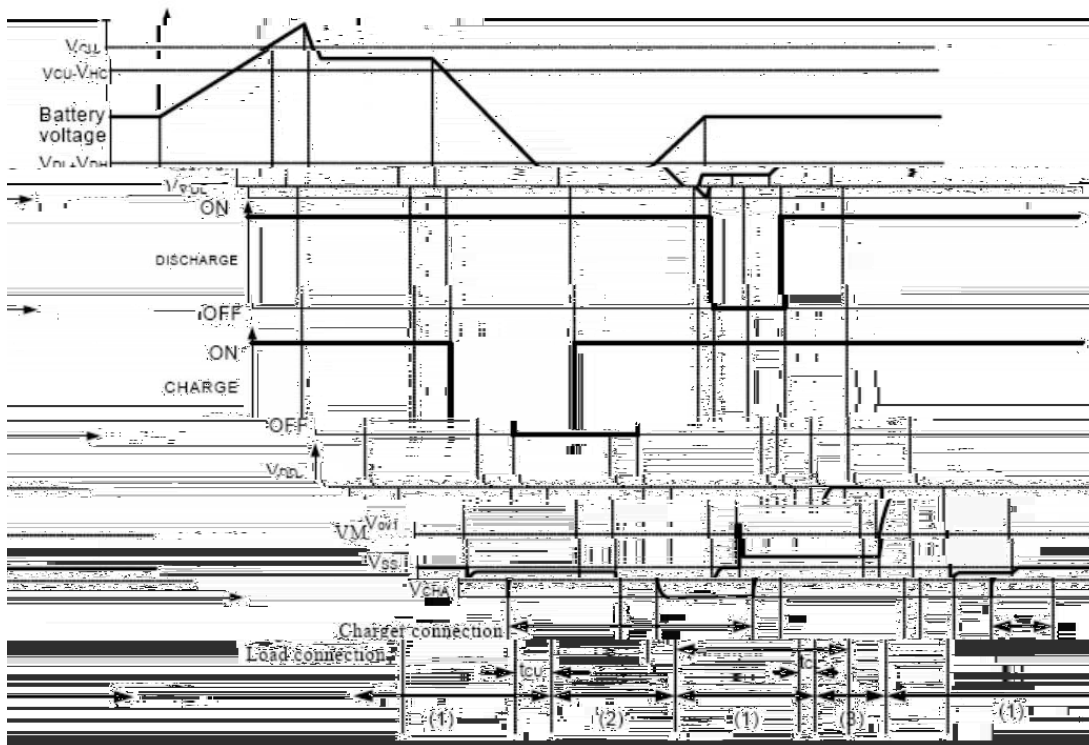
1 When the discharging current becomes equal to or higher than a specified value (the VM pin voltage is

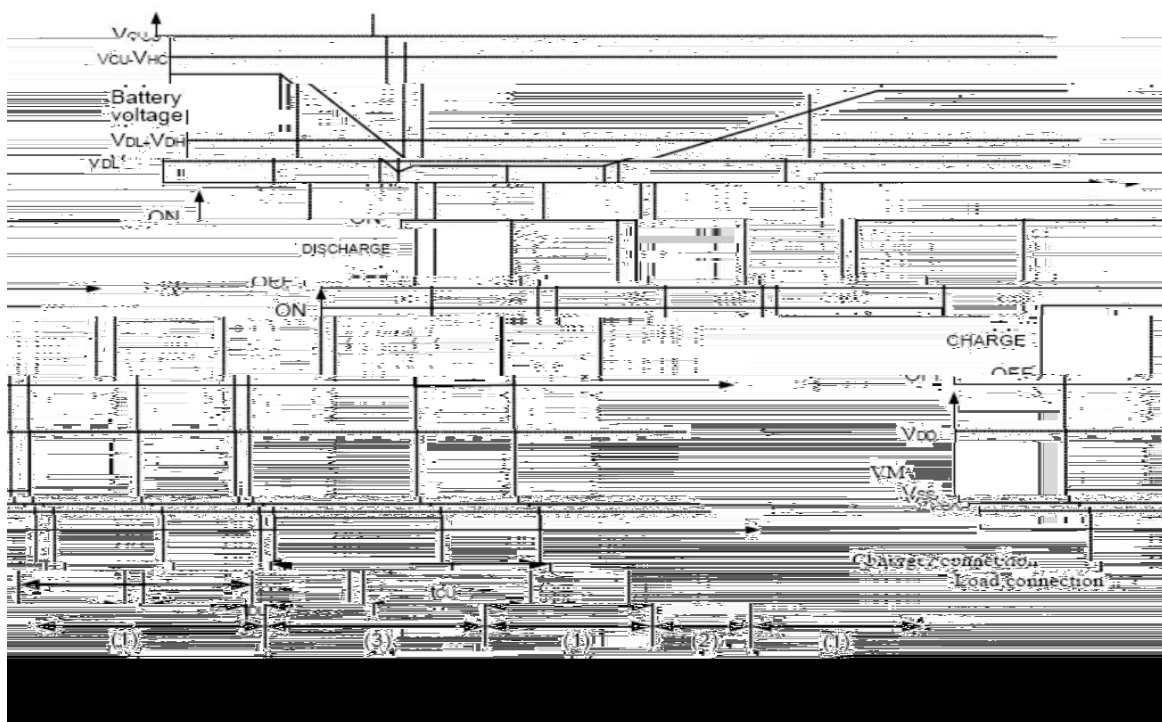
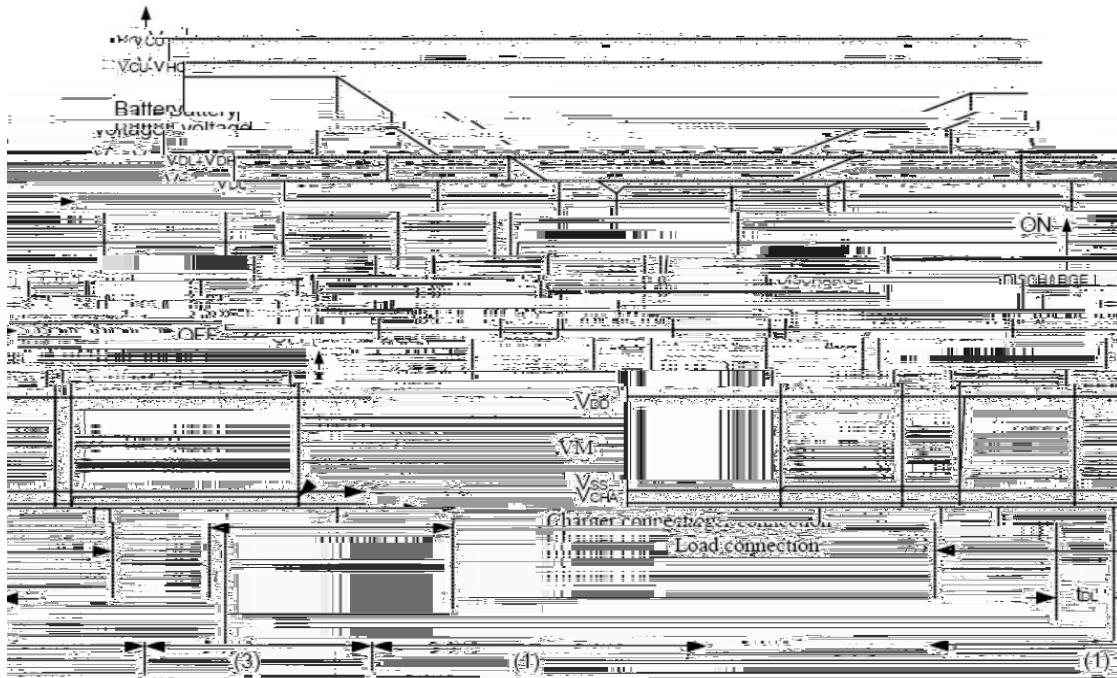


	VM		(VCHA)		
(TCU)	BRCL3130ZF	FET			
	VM GND		(VCHA)	0V	0V

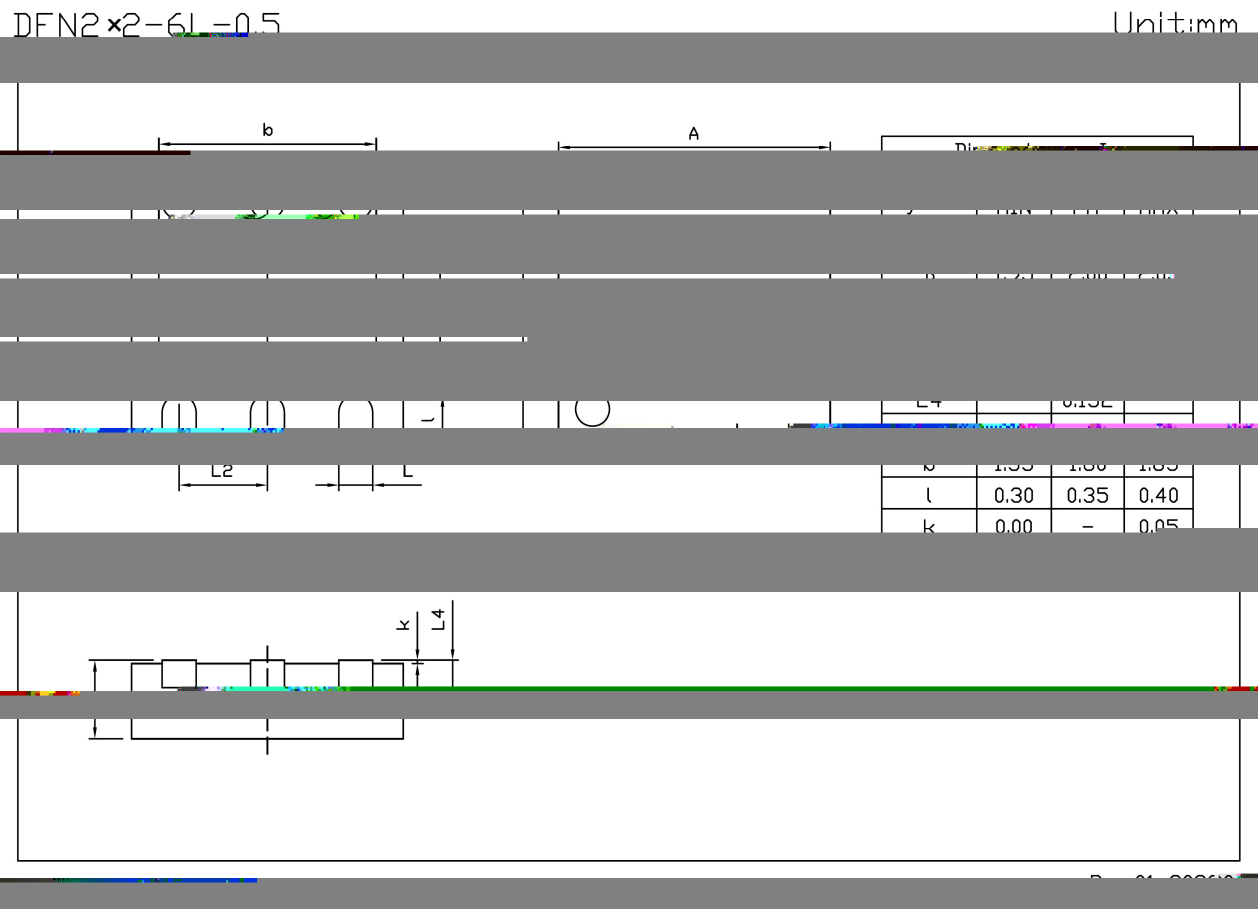
If the VM pin voltage drops below the charger detection voltage (VCHA) during charging under the normal condition and it continues for the overcharge detection delay time (TCU) or longer, the BRCL3130ZF turns the charging control FET off and stops charging. This action is called abnormal charge current detection.

Abnormal charge current detection is released when the voltage difference between VM pin and GND pin becomes higher



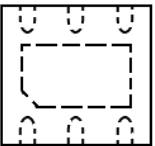


Notes: (1) Normal condition (2) Overcharge voltage condition (3) Overdischarge voltage condition (4) Overcurrent condition

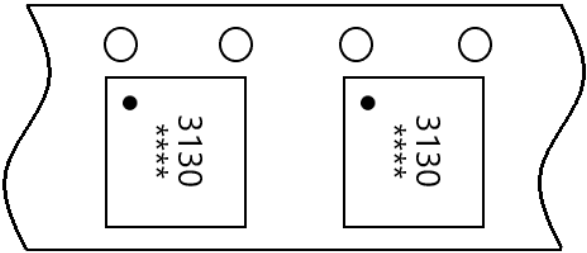




正面俯视图



正面透视图



编带示意图

3130

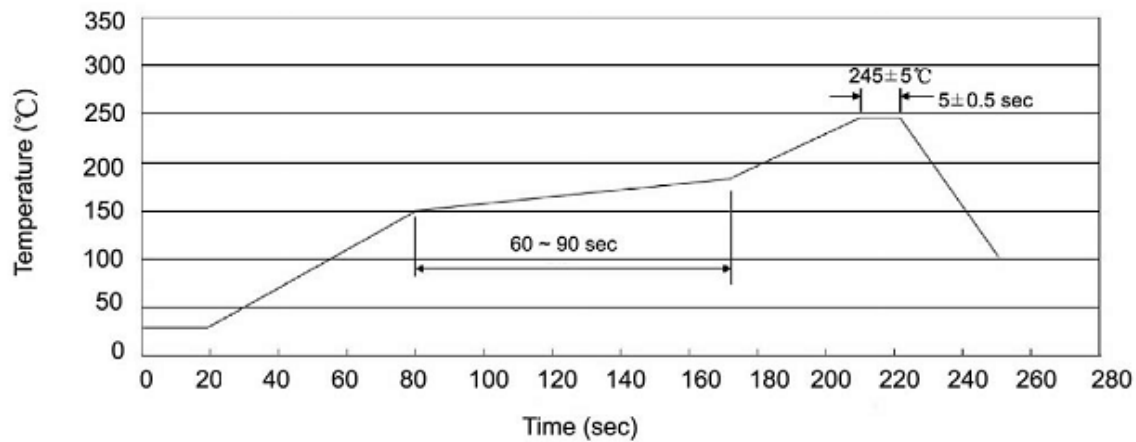
Note:

3130:

Product Type.

****:

Lot No. Code, code change with Lot No.



- 说明：

 - 1、预热温度 150 ~ 180℃，时间 60 ~ 90sec;
 - 2、峰值温度 245±5℃，时间持续为 5±0.5sec;
 - 3、焊接制程冷却速度为 2 ~ 10℃/sec.
- Note:

 - 1.Preheating:150~180℃, Time:60~90sec.
 - 2.Peak Temp.:245±5℃, Duration:5±0.5sec.
 3. Cooling Speed: 2~10℃/sec.

温度：260±5℃ 时间：10±1 sec. Temp.:260±5℃ Time:10±1 sec

卷盘包装 / REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit : mm ³)		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
DFN2×2-3L/6L	4,000	10	40,000	4	160,000	7" ×8	210×210×205	445×445×230