

[illegible]

描述 / Descriptions

BRCL3230BZF / BRCL3230BZF

MOSFET

BRCL3230BZF DFN2×2-6L

BRCL3230BZF

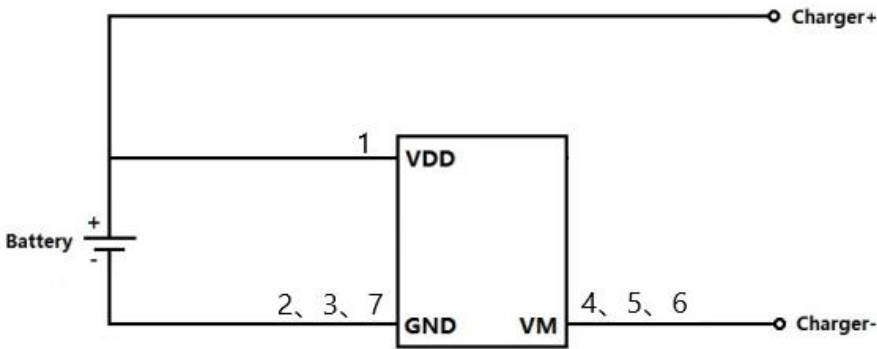
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特征 / Features

- ◆ 25mΩ MOSFET
- ◆ DFN2×2-6L
- ◆ RC
- ◆
- ◆
- ◆ 2 1 2
- ◆
- ◆ 0V
- ◆ 3.0uA, 1.7uA;
- ◆
- ◆
- ◆ ×
- ◆
- ◆
- ◆
- ◆
- ◆ ;
- ◆
- ◆
- ◆
- ◆

用途 / Applications

典型应用电路图 / Typical Application

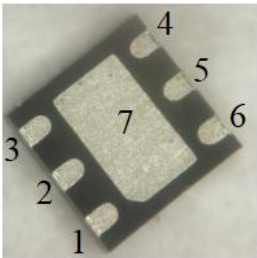


(1)

(2)

:

引脚排列 / Pinning



Pin Number	Pin Name	Pin Description
1		
2 3 7		
4 5 6		

印章代码 / Marking



极限参数 / Absolute Maximum Ratings(Ta=25℃)

			℃
			℃
			℃
			℃
			℃
			℃

电性能参数 / Electrical Characteristics(除非特别指定 , Ta=25℃)

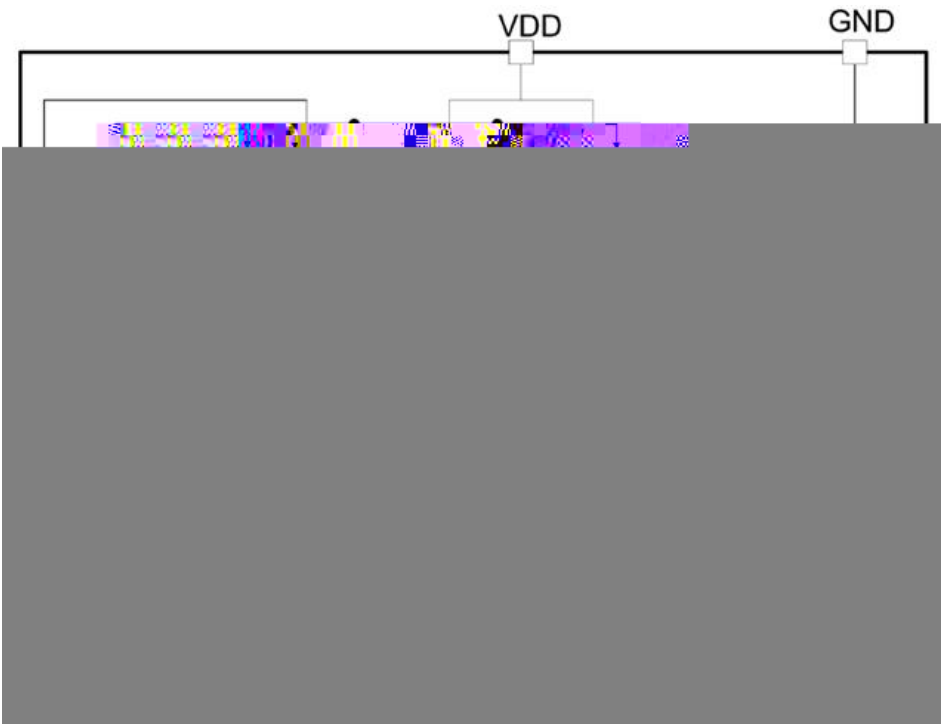
Detection voltage						
Detection current						
Static current						
		,				



电性能参数 / Electrical Characteristics(除非特别指定 , Ta=25℃)

Equivalent FET on Resistance						
		,				
Over temperature protection						
						℃
						℃
Delay time						

功能框图 / Functionl Block Diagram



BRCL3230BZF

MOSFET

25mΩ

正常工作模式 / Normal Operating mode

VCU ,

TCU

BRCL3230BZF

FET

1

VCL BRCL3230BZF

2

BRCL3230BZF

VM

0.7V

BRCL3230BZF

VCU

VCU BRCL3230BZF

VCU
VCU

VM

1

VCU

VCU

1 2

VCU

1 2

BRCL3230BZF

1

VM GND

1 VM

2 VDD RVMS

,BRCL3230BZF

VM FET

,VM GND RVMS

VM

VM

(TCU)	VM		(VCHA)	
	BRCL3230BZF	FET		
	VM	GND	(VCHA)	0V
			0V	

VM	VSHORT	tSHORT
BRCL3230BZF	VM	VSHORT

(1) 0V VDL IC

(2) " 0V " " 0V " VDL 0V "

(3) VM GND VM GND

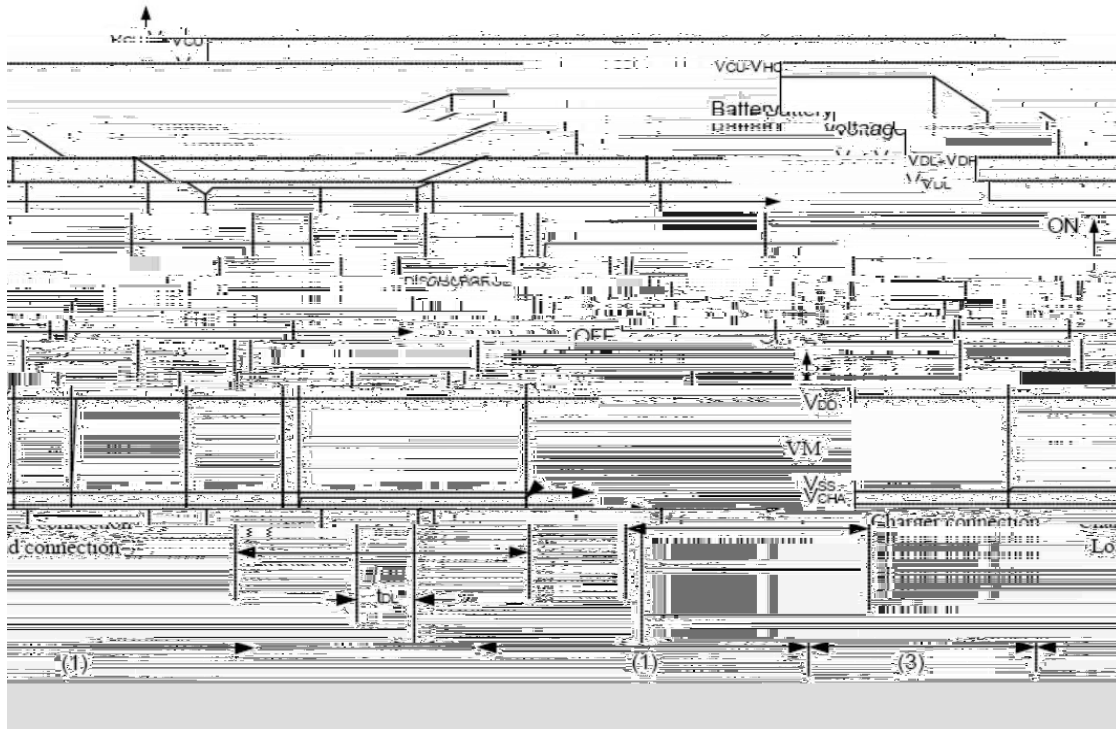
[illegible]

The timing diagram illustrates the sequence of events during a battery connection and discharge cycle. Key signals and parameters include:

- Battery**: Indicated by an upward arrow at the start of the sequence.
- V_{BAT}**: Battery voltage signal.
- V_{CHG}**: Charger voltage signal.
- V_{PL}**, **V_{OH}**, **V_{OL}**: Logic level signals for the charger connection.
- ON**: Signal indicating the charger is turned on.
- DISCHARGE**: Signal indicating the battery is in a discharge state.
- OFF**: Signal indicating the charger is turned off.
- V_{CHG2}**, **V_{SS}**: Additional voltage signals related to the charger connection.
- Charger connection**: A shaded region indicating the period when the charger is connected.
- Load connection**: A shaded region indicating the period when the load is connected.
- t_{CHG2}**, **t_{SHORT}**, **t_{OV}**: Timing parameters for the charger connection, short circuit, and overvoltage protection.
- t_{CHG2}**, **t_{SHORT}**, **t_{OV}**: Timing parameters for the charger connection, short circuit, and overvoltage protection.

时序图 / Timing Chart

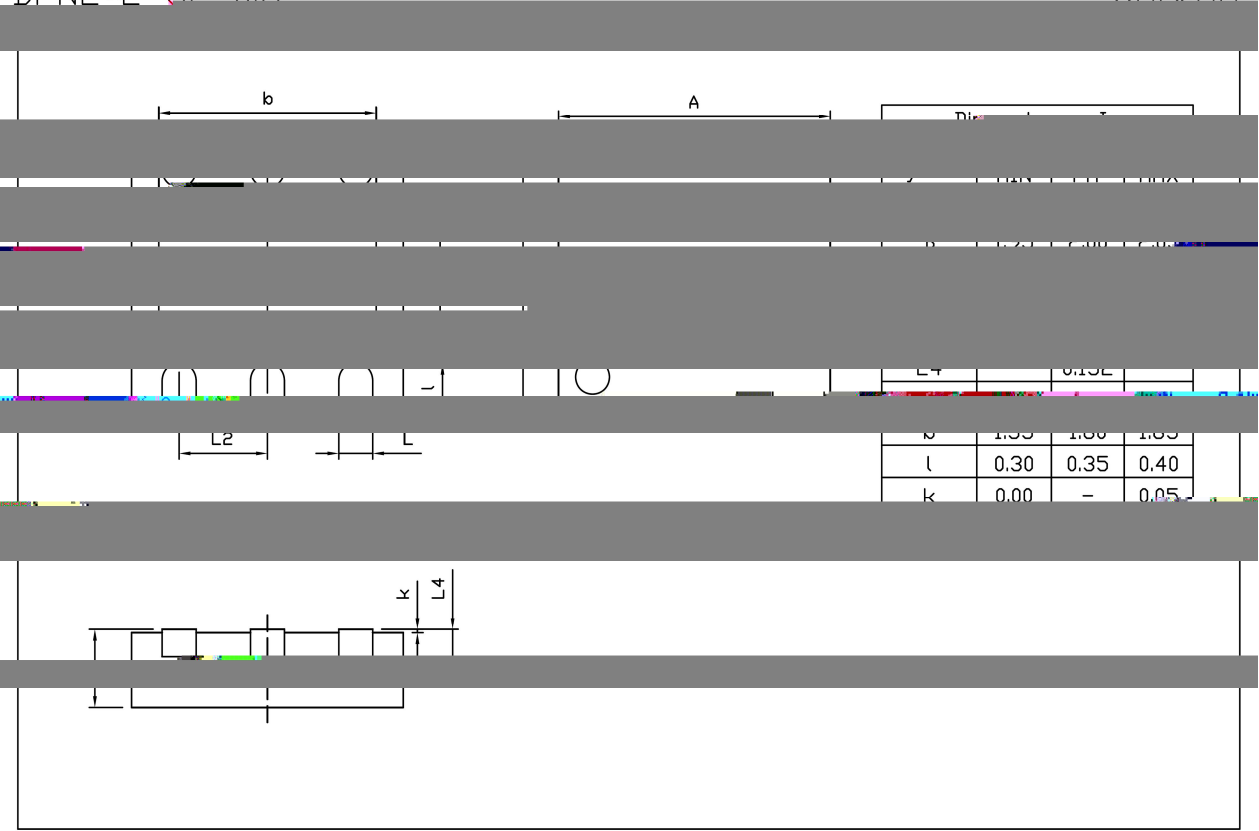
充电器检测/Charger Detection





外形尺寸图 / Package Dimensions

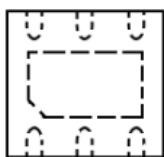
DFN2×2-6L-0.5 Unit:mm



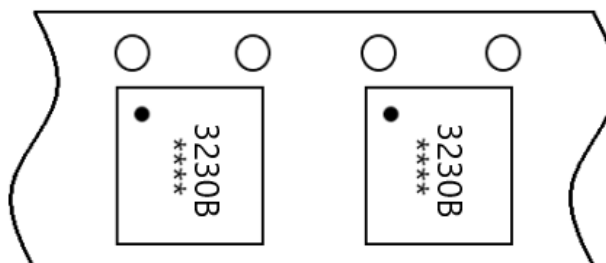
印章说明 / Marking Instructions



正面俯视图



正面透视图



编带示意图

3230B

BRCL3230BZF